

Analog Self-Interference Cancellation with Automatic Gain Control for Full-Duplex Transceivers

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Abstract—The main challenge in full-duplex transceiver design is the self-interference (SI). Analog SI isolation is performed at radio frequency (RF) by using an antenna design based on the characteristic modes theory and using active cancellation principle. Two different structures based on using a vector down-converter and a complex multiplier are used for analog baseband SI cancellation. Cancellers are tuned using a automatic gain control (AGC) enhanced variable-step steepest descent algorithm while transmitting a data signal to a distant node in half-duplex mode. Simulations show that the inclusion of AGC into the tuning process speeds up the convergence significantly.

Index terms – Full-duplex, self-interference, adaptive cancellation

I. INTRODUCTION

Full-duplex (FD) transceivers can transmit and receive simultaneously at the same carrier frequency offering the potential to double the spectral efficiency, to reduce air interface delays, and to facilitate improved collision detection and avoidance mechanisms in content based networks. Other benefits as well as challenges in the FD technology development have been reviewed in, e.g., [1], [2], [3].

The main problem in the FD transceiver design is the self-interference (SI), i.e., the leakage of the transmit signal to the device's own receiver. Depending on the system, the SI cancellation requirement can be well over 100 dB. In order to achieve such a high isolation levels, the SI cancellation must be done at different stages. Reviews on different SI cancellation techniques can be found, e.g., in [4], [5], [6]. In this paper, the self-interference cancellation is performed with three techniques: antenna isolation, active cancellation at radio frequency (RF) and analog cancellation at the baseband. Since the attenuation and phase settings of the active cancellation can change during the operation, they must be tunable. The usage of a gradient descent algorithm for the tuning has been proposed in [7]. Designs reported in [8], [9] also use iterative algorithms to adjust the phase and attenuation in the feed forward paths of the analog SI cancellers. The tuning of the SI cancellation in [7], [8], [9] is done prior the full-duplex communication. Designs in [8], [9] are front end-designs where the tuning algorithms are run on a specific control unit and an additional analog-to-digital (AD) conversion is needed for the SI cancellation. In our design, all the processing is performed at the baseband processing unit, hence no additional

AD converters are needed for the SI cancellation. The tuning of the RF and baseband cancellation circuitry is done in the half-duplex (HD) mode using the transmitted data signal without the need to use a specific tuning signal, i.e., the data transmission can be continuous. Tuning is performed with the steepest descent algorithm. Automatic gain control (AGC) is used to scale the input signals of AD converters to fully utilize the dynamic range of the converters. Simulations show that this speeds up the convergence of the tuning considerably. After the tuning is completed, the system can be switched to the FD mode.

The SI channel consists of the leakage through the antenna and the reflections from the operating environment of a FD transceiver. Although there are some published measurements of a SI channel [10], [11] there are no widely accepted models for the SI channel. A SI channel modeling based on a ray tracing approach is proposed herein in Section III. It allows a relatively simple way to model a multipath SI channel.

Main contributions considered are the inclusion of the AGC in the tuning process, introduction of the analog complex multiplier based baseband cancellation and the SI channel modeling using a ray tracing approach. The rest of the paper is organized as follows. The transceiver model and the SI cancellation are described in Section II, SI channel model is presented in Section III and numerical results are presented in Section IV. Finally, conclusions are presented in Section V.

II. TRANSCEIVER AND SELF-INTERFERENCE CANCELLATION

A block diagram of a full duplex transceiver is shown in Fig. 1. Four different techniques for the SI cancellation are included in the architecture: isolation provided by the antenna, SI cancellation at RF, SI cancellation at analog baseband and digital cancellation. Antenna isolation, RF cancellation and analog baseband cancellation are considered. Antenna design is based on the characteristic modes theory [12], [13]. The same antenna model was used also in [14], [15]. The RF canceller is shown in Fig. 2. It consists of a FIR filter type structure where the phase and gain values of each tap can be tuned. The delay τ equals the sample time of the analog-to-digital (AD) converters. Two different structures for the analog baseband SI cancellation are presented in

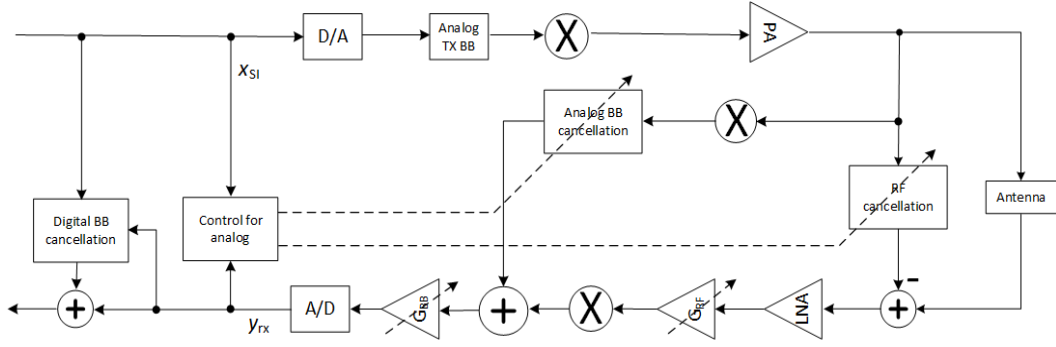


Fig. 1. FD transceiver architecture.

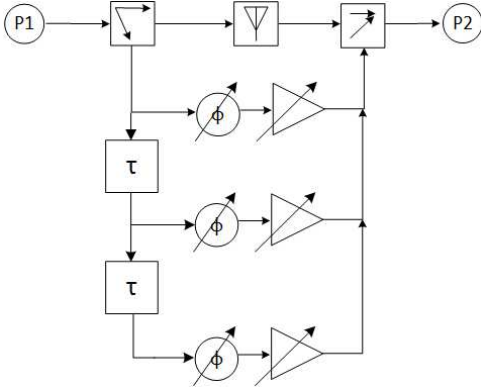


Fig. 2. Antenna and RF SI cancellation.

Fig. 3. The first one uses vector down converter to take into account the phase shift caused by the SI channel, the other uses analog complex multiplier to compensate for the phase rotation. The vector down converter based canceller requires the phase to be estimated. Since its output is a non-linear function of the phase, its tuning is more complicated than the tuning of the complex multiplier. In the complex multiplier, the output is a linear function of the gain values that need to be estimated. Only one-tap baseband cancellers are considered. The complex multiplier based canceller can be extended to multi-tap structure for multipath SI channel cases but that requires the inclusion of tunable delay elements into the canceller and the usage of an algorithm to estimate the delay and gain values of an multipath channel.

Since changes in the environment can cause the SI channel to change, the SI cancellers must be tunable. The tuning of the analog cancellers is done with a transmitted data signal in a half duplex mode, i.e, no FD specific training signals for the tuning are needed. Both the RF and baseband cancellers are tuned using a steepest descent algorithm. At each iteration step, a complex coefficient vector $\mathbf{w}(k)$ is calculated as

$$\mathbf{w}(k) = \mathbf{w}(k-1) - \frac{\mu_i}{MP_x} \sum_{n=1}^M y_{rx}(n) \mathbf{x}_{SI}^*(n), \quad (1)$$

where k is the iteration index, M is the number of samples per

iteration, P_x is the power of the SI signal x_{SI} , y_{rx} is the received signal and μ_i is the step size of the algorithm. The numerical values μ_i depend on the properties of the signal as well as on the properties of the transceiver, especially the gain of the receiver, and they have been selected by simulations to allow fast convergence at the beginning and good accuracy at the end of the tuning. Vector \mathbf{x}_{SI} consists of samples $x_{SI}(n), x_{SI}(n-1) \dots x_{SI}(n-N)$ for a N length SI canceller.

Since the SI level is reduced during the tuning, the signal at the input of AD converters is reduced. In order to utilize the full dynamic range of the AD converters during the tuning, AGC is added to the model. In the case of the RF canceller the gain of the amplifier G_{RF} in Fig. 1 is controlled using Algorithm 1. In baseband cancellation case the G_{BB} is controlled with the same algorithm.

Algorithm 1

if $P_{rx} > P_{th}$ **then**

$$G_{RF/BB}(k) = G_{RF/BB}(k-1) - 2 \text{ dB}$$

else if $P_{rx}(k) < P_{rx}(k-1) - 3 \text{ dB}$ and $G_{RF/BB} < G_{RF/BB,max}$ **then**

$$G_{RF/BB}(k) = G_{RF/BB}(k-1) + 2 \text{ dB}$$

end if

P_{th} is a threshold power for preventing ADC overload. It was shown in [14] that using a variable step size in the SD algorithm can improve the convergence rate. In this case, the step is chosen based on the AGC value using Algorithm 2.

Algorithm 2

if $G_{AGC} < G_{AGC,th}$ **then**

$$\mu_i = \mu_1$$

else

$$\mu_i = \mu_2$$

end if

III. SELF-INTERFERENCE CHANNEL MODEL

The SI channel consists of the direct coupling through the antenna and reflections from the environment. Reflections are modeled using a ray tracing approach. The attenuation of a reflection path i is

$$L_i = L_{free}^i + L_R^i - G_{tx}^i - G_{rx}^i, \quad (2)$$

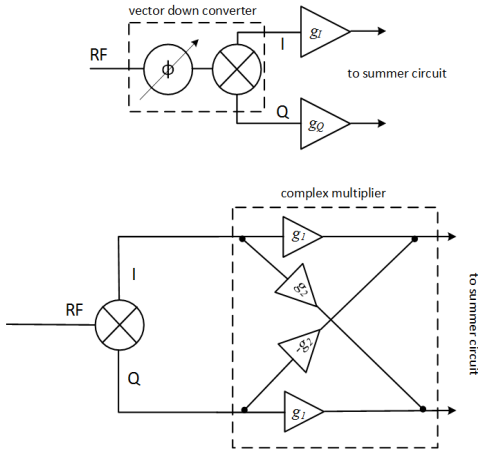


Fig. 3. Analog baseband SI cancellation.

where L_{free}^i is the free space loss of the i^{th} path

$$L_{\text{free}}^i = 20 \log_{10}(2d_i) + 20 \log_{10} f + \log_{10} \frac{4\pi}{c}, \quad (3)$$

d_i is the distance in meters between the transceiver and reflecting surface, f is the frequency in herz and c is the speed of electromagnetic radiation. The loss component caused by the reflection is

$$L_{R_i} = 20 \log_{10} R_i, \quad (4)$$

where the reflection coefficient R_i is [16], [17]

$$R_i = \frac{\sin \theta_i - Z_i}{\sin \theta_i + Z_i}, \quad (5)$$

and [16], [17]

$$Z_i = \begin{cases} \sqrt{\frac{1}{\epsilon_{r_i}^2} (1 - \cos^2 \theta_i)} & \text{(vertical polarization)} \\ \sqrt{\epsilon_{r_i} - \cos^2 \theta_i} & \text{(horizontal polarization)}. \end{cases} \quad (6)$$

ϵ_{r_i} is the relative permittivity of the i^{th} reflecting surface and θ_i is the grazing angle of i^{th} reflection. G_{tx}^i and G_{rx}^i in (2) are the gains of the antenna in the transmit and reception direction of the reflections. Since the i^{th} reflection path is assumed to include only one reflection, these gains are equal but the ray tracing approach can also be extended to include multiple reflections in a single multipath component. The relative permittivity of some of the construction materials are listed in Table I. In the same table reflection losses with some grazing angles are also given.

IV. NUMERICAL EXAMPLES

The signal used in the simulations is an orthogonal frequency-division multiplexing (OFDM) signal with 48 data sub-carriers and 4 pilot sub-carriers at 3.5 GHz center frequency. Data sub-carriers are modulated using 16 level quadrature amplitude modulation (16-QAM). The bandwidth of the signal is 20 MHz. The antenna is modelled using an electromagnetic simulation tool (CST Microwave Studio). Simulated S-parameters are brought to the system model

as a S-parameter file. The isolation of the antenna over the signal bandwidth is 61 dB. The FD transceiver including the analog SI cancellers and AD converters is modelled using the Advanced Design System (ADS). The maximum values of the integral (INL) and differential (DNL) non-linearity to least significant bit of the AD converters are 5.0 and 0.7, respectively. The phase and amplitude imbalance of the transceivers are 0.5° and 0.1 dB, respectively, and the phase noise data is taken from [18], [14].

Case 1: No Reflections from the Environment

Firstly, the SI consists of only the signal leaking through the antenna from the transmitter to the receiver. This means that the SI channel consists of the transmitter chain, antenna and receiver chain. The used antenna provides 61 dB of isolation over 20 MHz bandwidth and RF and analog baseband circuitry can be used to improve the isolation.

Case 2: Multipath SI channel

Secondly, the SI consists of the signal leaking through the antenna and signals reflecting back from the objects or the surfaces in the surrounding environment of the FD transceiver. The antenna cannot attenuate the signal components reflecting the surface. Hence, the isolation provided by the antenna is lower than in the Case 1. The SI channel has 3 paths caused by the reflections in addition to the leakage through the antenna. The reflections are modeled using the ray tracing approach described in Section III. The reflecting surfaces are assumed to be at the distances of 0.5 m, 1 m and 3 m from the transmitter. The reflection loss is assumed to be 8 dB, and antenna gain is 3 dB. This gives the delay attenuation pairs (3.33 ns, 45 dB), (6.67 ns, 50 dB) and (20 ns, 55 dB) for the paths. In addition, a 180 degree phase shift is assumed for all the reflections. With these reflections the antenna isolation is decreased to 49 dB.

A. RF Cancellation

The performance of a 1-tap RF canceller is shown in Fig. 4. The vertical axis shows the amount of cancellation in decibels (dB) provided by the canceller and the horizontal axis shows the number of iterations needed for the tuning (k in Algorithms 1 and 2). The SIC_RF_NO_ref curve shows the performance in the Case 1 and the SIC_RF_nref curve the Case 2. The total isolation after the convergence in the Case 1 is $25 + 61$ dB and in the Case 2 $18 + 49$ dB.

The SI cancellation performance of a 3-tap canceller in Case 2 is shown in Fig. 5. The SIC_RFn1 curve (blue line with circles) and the SIC_RFn curve (red) show the performance when the step size μ of the SD algorithm is $10 \cdot 10^{-3}$ and $15 \cdot 10^{-3}$, respectively. As can be seen, the step size can be used to control the convergence rate of the tuning but the tuning is slow. In both of these cases the gains G_{RF} and G_{BB} chain has been constant and it has been set so that the AD converters do not limit the peaks of their input signals. The black SIC_RFn2 curve with 'x' markers shows the performance when the AGC has been used to control the gain G_{RF} and the step size is selected based on the gain value. The usage of the AGC and

TABLE I
REFLECTION LOSSES CAUSED BY SOME CONSTRUCTION MATERIALS

Material	ϵ_r	Vertical polarization					Horizontal polarization				
		30°	45°	60°	75°	90°	30°	45°	60°	75°	90°
Glass window	6.5	-16.4	-10.3	-8.3	-7.4	-7.2	-3.7	-5.2	-6.3	-7.0	-7.1
Concrete (dry)	4.5	-22.5	-12.8	-10.2	-9.2	-8.9	-4.6	-6.4	-7.8	-8.6	-8.9
Chipboard	2	-25.1	-22.9	-17.5	-15.8	-15.3	-8.4	-11.4	-13.6	-14.9	-15.3

variable step SD speeds up convergence substantially. After the convergence the RF canceller provides 34 dB of additional SI attenuation (34 + 49 dB in total). RF cancellers in [8], [9] provide about (30) dB of cancellation. However, it should be taken into account that the results in [8], [9] are from real measurements. Simulation results shows that it is possible to achieve good SI cancellation performance using the data signal for the tuning and without the need for independent front end designs where tuning are run on a specific control unit with additional AD converters.

B. Analog Baseband Cancellation

The performances of the both analog baseband cancellers in the Case 1 are shown in Fig. 6. The SIC_I and SIC_Q curves show the convergence and SI cancellation at the I and Q branches of the receiver before the AD converters when the vector down converter followed by two tunable amplifiers are used. At the beginning of the tuning, the amplifier gains are set to zero. This means that the canceller provides no additional cancellation at the beginning. The phase shift of the vector down converter is set to the phase shift of the antenna at the center frequency. This value is taken from the known S-parameter model and it is not changed during the tuning. Amplifiers at the I and Q branches of the canceller are tuned independently using the received I and Q signals after the AD converters. Tuning is done using the SD algorithm with constant step size.

SIC_BB_I and SIC_BB_Q curves show the performance of the analog complex multiplier based SI cancellation. The gains of the canceller are set to zero at the beginning of the tuning and the tuning is done using the received complex baseband signal after the AD converters. Tuning is done using the SD algorithm with constant step size and AGC is used to control the gain of amplifiers G_{BB} following the SI canceller. As can be seen, both structures give the same performance of after the tuning but the complex multiplier based canceller has converged much faster than the vector down converter based structure. The curves in the Fig. 6 show the additional SI attenuation provided by the baseband cancellers. The total SI cancellation is the sum of the antenna isolation and the additional cancellation shown in the figure (= 30 + 61 dB).

Results of analog baseband cancellation have been reported in [19], [20]. Measured baseband SI cancellation performance in those papers has been about 20 dB. A vector down converter is used in [19] to rotate the phase of the SI signal used for the cancellation. The signal at the input is first attenuated at RF and then down converted using a vector down converter whereas here the level of the signals at the I and Q branches

are independently controlled after the down conversion. In [20] the baseband canceller uses a 14-tap filter for the analog cancellation. No details are given about the tuning of the circuit but since its performance is at the same level as in [19] and lower than reported in this paper, it is possible that the tuning has converged into the state where only one tap is contributing to the cancellation.

The complex multiplier based canceller was tested also in the Case 2. The performance in this case is presented with curves SIC_BB_I1 and SIC_BB_Q1. As can be seen the baseband canceller gives 20 dB of additional cancellation to the antenna isolation (total isolation 49 + 20 dB).

V. CONCLUSIONS

A SI channel model consisting of the leakage path through the antenna and reflections from the environment was introduced. Reflection paths were modeled using a ray tracing approach, where the reflection losses are calculated based on the electrical properties of the reflecting surfaces. The performances of RF and analog baseband SI cancellation were evaluated with simulations. The inclusion of the AGC into the tuning process speeds up the convergence significantly. In the simulated multipath SI channel case, the RF canceller provides 30 dB of additional cancellation when compared with the case where only the antenna is providing isolation between the transmitter and receiver.

Two structures for 1-tap analog baseband SI cancellation were compared. The first one uses vector down-converter to compensate for the phase shift of the SI channel, the second one utilizes complex multiplier structure. They both provide about the same isolation after the tuning but the complex multiplier based solution is easier to tune and it converges very rapidly. When there are no reflections from the environment the baseband cancellation gives better performance than the 1-tap RF canceller and it is also able to provide additional isolation in the multipath case.

ACKNOWLEDGMENT

The research has been partially funded by Tekes - the Finnish Funding Agency for Innovation, Nokia Oyj, Esju Oy and CoreHW

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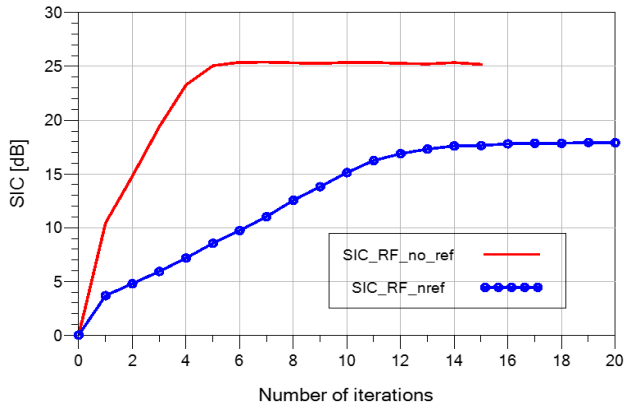


Fig. 4. Performance of the 1-tap RF SIC.

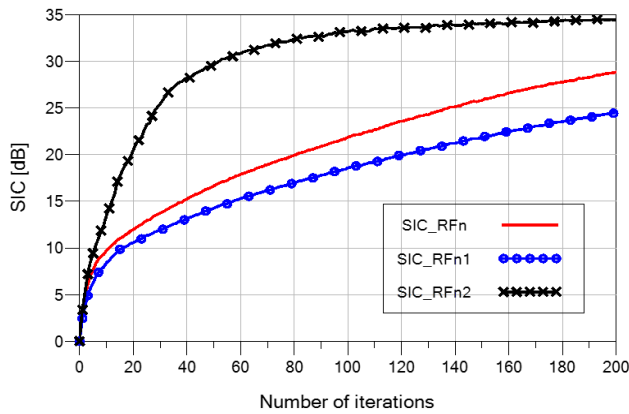


Fig. 5. Performance of the 3-tap RF SIC.

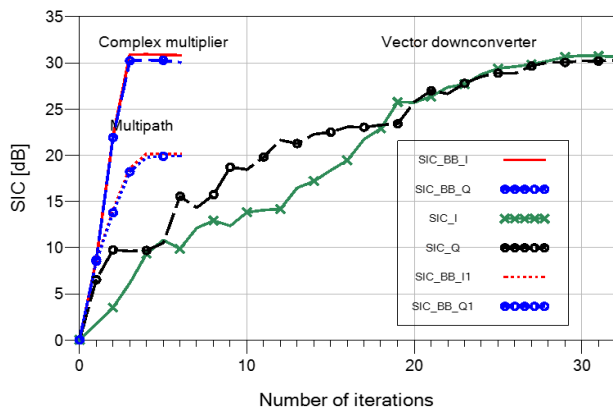


Fig. 6. Performance of the analog baseband SIC.

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