Abstract—This work suggests a novel procedure of calibrating timing mismatch of time-interleaved Analog-to-Digital conversion. Here, the timing mismatch calibration and compensation are both performed digitally. The proposed method is simple and it is able to find timing errors below the Nyquist rate of the individual analog-to-digital converter. Moreover, the method can be easily modified for digital gain error calibration.

Index Terms—time-interleaving, ADC, mismatch calibration

I. INTRODUCTION

The purpose of time-interleaving technique in analog-to-digital (A/D) conversion is to lower the sampling rate in A/D interface. Time-interleaved architectures exploit parallelism to enable sampling rates that are not achievable (or power inefficient) in single-channel A/D converters [1]. By interleaving the data into N channels or paths, the conversion is performed by N individual A/D converters operating at $f_s/N$, $f_s$ being the data rate of the combined time-interleaved A/D converter (ADC) output. Well-known drawbacks of a time-interleaved ADC (TI-ADC) are caused by the mismatches between individual paths.

The main mismatches in TI-ADC are gain error, offset error and timing error. Gain and offset errors correspond to mismatches between ADCs and timing error refers to sampling time skew i.e. small aperture delay mismatches between paths.

This work focuses on calibrating timing mismatches in from the digital output of interleaved data. The correction (error compensation) will be performed to the digital data. The proposed method falls in the category of blind background calibration i.e. no offline training stimulus (pilot input) will be used.

In [2], Vogel and Johansson reviewed several publications on timing mismatch calibration/correction and concluded that the goal of timing mismatch correction method for in TI-ADC is to find an accurate, power-aware method, which only needs a slight oversampling and which can be easily adapted to changing timing mismatches. This work suggest a method that does not require oversampling of the individual Sub-ADC.

In [3], Jamal et al. proposed adaptive timing error calibration for a 2-path (2-channel) TI-ADC. Their strategy was to upsample both channels by N (number of paths) in order to synchronize the paths that for the adaptive feedback loop. The learning data from adaptive filtering block was obtained from spurious tone, limiting the Sub-ADC data bandwidth to be less than the Nyquist band, as in e.g. [4], [5]. In [6], Law et al. expanded the idea in [3] to a four-channel TI-ADC.

The method proposed in this work uses the idea of channel synchronization as in [3], [6], but without upsampling and with a different adaptation logic for the timing error mitigation. The suggested digital calibration scheme is capable of calibrating timing errors even from above Nyquist bandwidth of Sub-ADCs.

Section II discusses the oversampling ratio and demonstrates overlapping spurious tone clusters (i.e. birdies) in TI-ADC, due to low data rate. Section III introduces the proposed calibration method and the optimal parameters are discussed in Section IV. Section IV also discusses the performance of the proposed calibration method.

As the focus in this work is at the timing mismatch calibration, it is noteworthy that the proposed digital calibration method is insensitive to the gain and offset errors. This means that the gain and offset error calibration can be done in parallel with timing mismatch calibration. The proposed method can be modified to calibrate gain mismatch and this will be presented in Section V.

II. SUB-ADC OVERSAMPLING RATIO

The spurious tones due to TI-ADC mismatches (a.k.a. birdies) were analyzed by Kurosawa et al. in [7]. Gain, offset and timing errors between Sub-ADCs cause spurious images and spikes centered at $f_{c_{spur}}$ according to the number of paths, as shown in (1).

$$f_{c_{spur}} = k \cdot f_s/N, \quad \text{where} \quad k = 1, 2, \ldots, N \quad (1)$$

The characteristic birdies due to time-interleaving are demonstrated in Fig. 1, where we have a 12-bit 7-path TI-ADC with a multi-tone (11 tones) as a stimulus. Figure 1 depicts three different mismatch mechanisms: offset (Fig. 1a), gain (Fig. 1b) and timing mismatch (Fig. 1c). The oversampling ratio (OSR) for Sub-ADCs in Fig. 1 is two, i.e. the data rate of each Sub-ADC is twice the Nyquist rate.

If the sampling rate of Sub-ADCs is less than the Nyquist rate, the spurious tone clusters in the TI-ADC output will alias and tend to overlap with each other and with the fundamental. If we reduce the OSR from 2 to 4/7, the data in the Sub-ADC is undersampled, see Fig. 2.
The combined TI-ADC output sampling rate ($f_s$) is N-fold compared to a Sub-ADC, which means that the TI-ADC output sampling rate shown in Fig. 1 and Fig. 2 will also be N-fold. To avoid confusion, the term OSR will be here tied to the Sub-ADC. The N-fold TI-ADC oversampling ratio will be denoted as samples-per-symbol (sps). The connection between sps, OSR and Sub-ADC bandwidth (BW) are shown in (2) and (3).

$$\text{OSR} = \frac{sps}{N} \quad (2)$$

$$\text{BW} = \frac{f_s/N}{2 \cdot \text{OSR}} = \frac{f_s}{2 \cdot \text{sps}} \quad (3)$$

In the art of digital mismatch calibration of an individual channel, low oversampling ratio poses challenges. If part of the spectrum is dedicated as a spurious tone measurement, it is imperative that the measurement band has no fundamental and/or aliased fundamental. Digital fractional delay filters (baseband FIR or IIR) may also be problematic as they are known to be less accurate near $f_s/2$ [8].

### III. Digital Timing Error Calibration

The structure of the proposed background calibration scheme is shown in Fig. 3. Thick data-flow arrows in Fig. 3 represent two or more data paths in parallel. Input data are quantized outputs of N paths operating at $f_s/N$.

The Correct block contains a fractional delay filter that delay or advance the data according to the input delay (which is initially all zeros). The fractional delay inputs in the Correct block originate from adaptive LMS block. These delays change by sampling instant until the delays converge.

The Sync block synchronizes the timing of all N paths by fixed fractional delay filtering. Basically, one path will be declared as a path with no aperture delay mismatch. The timing mismatches in other paths will be adapted until convergence. The consequence of declaring no timing error for one path is that there will be an additional aperture delay common to all delay estimates. This added aperture delay will only contribute to the overall delay, not the birdies.

The adaptation in Fig. 3 has properties that will cause fluctuation in LMS tap convergence. The fluctuation will be smoothed by a simple running average filtering for all N-1 LMS taps. The taps are negative timing mismatch estimates to be used in the compensation (Correct block).

#### A. Channel Synchronization

The Sync block in Fig. 3 synchronizes the data path delays between N paths at the Sub-ADC sample rate of $f_s/N$. The Sync block is detailed in Fig. 4, where the data path goes through fractional delay filters with fixed delays.

The reference point of the synchronization will be taken from the most delayed data path. Assuming the most delayed data is $y_N[n]$, the delay of $y_{N-1}[n]$ and $y_N[k]$, compared with $y_N[n]$ is:

$$d_k = \frac{N - k}{N}, k = 1, 2, \cdots, N \quad (4)$$

The synchronization of the data paths $y_1[n] \cdots y_{N-1}[n]$ cancels the delays in (4). The cancellation will be performed by fixed-delay fractional-delay FIR filters. Choosing a fractional delay filtering prototype function has many alternatives [8], which are discussed in Section IV.

As mentioned in the Introduction, [3], [6] had similar synchronization idea but at full sample rate of $f_s$, which results in integer delay synchronization.

#### B. Adaptation

After synchronization, [3], [6] suggested adaptive filtering for the timing error detection. Their strategy was to isolate the birdy responsible to timing mismatch and use it as the noise reference. In this work, the calibration is based on correlating
the baseband fundamental between a reference path and the other paths.

By not using spur-related noise reference, we are able to experiment an additional lowpass filter after the fixed fractional delays in Fig. 4. The additional lowpass-filter is the key on enabling timing error mitigation of subsampled Sub-ADCs below Nyquist rate: the in-band birdies can be filtered by some extent. Proper cutoff frequency for the lowpass-filter depends on the oversampling ratio of the Sub-ADC. The cutoff and the conditions under which the lowpass filter is beneficial are discussed in Subsection IV-B.

The adaptive logic in Fig. 5 is based on the simple and robust Least Mean Squares filtering [9]. The upper input to the multiplier in Fig. 5 represents the error whose mean squared error will be minimized. This input is the data \(-y_k(n)\) filtered by \((1 - z^{-2})\). Such filtering adds a notch at dc and \(f_s/2\), which cancels the fractional delay output at its most error-prone frequency range i.e. near \(f_s/2\). This means that the fractional filtering errors at \(f_s/2\) will not hinder the calibration.

The notch at dc due to \((1 - z^{-2})\) filtering cannot be avoided and it will contribute to LMS tap fluctuation, whose frequency is dependent on the calibration loop delay. As mentioned, the running average filtering in Fig. 3 will cancel the fluctuation.

The lower multiplicand in Fig. 5 is the reference data \(y_N(n)\), which will be delayed by one sample. This will synchronize the multiplicands.

By using a behavioral model, the choice of LMS correction can be tested without an adaptation feedback. Suppose you have simulated long arrays (length L) of synchronized path outputs (5). The matrix inversion in (6) with the reference array results in two coefficients, whose subtraction (7) results in (negative) approximated mismatch. The result correlates with the actual timing mismatch.

\[
y_k = [y_k(1), y_k(2), \cdots y_k(L)]^T \tag{5}
\]

\[
C_k = [-y_k(n), y_k(n - 2)]^{-1} \cdot y_N(n - 1) \tag{6}
\]

\[
-d_k = C_k(1) - C_k(2) \tag{7}
\]

As known in the theory of LMS algorithm, the adaptation constant \(\mu\) in Fig. 5 dictates the speed and accuracy of the learning curve. In theory, \(\lambda_{\text{max}}\) in (8) is the largest eigenvalue of the autocorrelation matrix of the input data \(y_N\) [10]. The additional filtering of the data also contributes to the maximal (and optimal) value if \(\mu\). The optimization of \(\mu\) is discussed in Section IV.

\[
0 < \mu < 2/\lambda_{\text{max}} \tag{8}
\]

C. Digital Timing Error Compensation

The LMS weights or taps in Fig. 5 are the negative timing errors that are fed into the Correct block shown in Fig. 3. A closer look at the Correct block is shown in Fig. 6. The taps are fed into a fractional delay Farrow structure [11], which performs the delay for the channels from 1 to \(N-1\). The Farrow structure enables delay filtering by varying fractional delays.

As the \(N^{th}\) path is the reference, it will be delayed by the overall delay of the fractional delay filters. For a FIR type fractional delay with the order of \(N_{\text{FIR}}\), the integer delay DF will be as in (9) [8].

\[
DF = \lfloor N_{\text{FIR}}/2 \rfloor \tag{9}
\]

IV. Optimization

As mentioned in the Introduction, a mismatch calibration and compensation method should be power-aware. For the calibration method suggested here, the design trade-offs (complexity and power consumption vs. convergence and performance) would concentrate on the fractional delay filter (continuously variable Farrow structure [11]) and the additional lowpass filter.

However, this work does not concentrate on a single fixed system so the focus is on performance optimization. The aim in this Section is to demonstrate that the proposed method is functional with any number of paths and different oversampling ratios.
A. Fractional Delay Filtering

The synchronization and correction blocks in Figures 4 and 6 both contain fractional delay filtering. There are many prototype FIRs to choose from, and the author favored these candidates:

- 3-parameter optimal offset window [12]
- Direct Least Squared Integral Error FIR Design [13]

The above prototypes were found suitable for both fixed delay filtering (Fig. 4) and variable-delay Farrow structure [11] (Fig. 6). The Farrow structure [11] is a collection of polynomially fitted filter coefficient arrays from which the proper filter coefficients are evaluated according to the delay path. The true timing mismatches are, of course, taken from a behavioral model. The maximal value of $\mu$ in (8) is naturally dependent on the type of stimulus.

In the sweeps discussed below and in Section IV-C, a 17-tone stimulus was used with maximal value of 90% of full scale. The optimization results for $\mu$ reported below will therefore be suited with stimuli with high peak-to-average power.

$$R = \frac{\text{cov}(d, \hat{d})}{\sigma(d)\sigma(\hat{d})} \quad (10)$$

The optimized cutoff frequencies and the LMS step size $\mu$ for the case of FIR filter are shown in Fig. 7. For IIR filter, the graphs were very similar and are not depicted here. The initial guess for the cutoff frequency (shown in Fig. 7a) is defined in (11) and (12). Not that the optimized $f_c$ are very close to the initial values, especially for OSR > 1.

$$f_c = \frac{\text{BW}}{2}, \text{OSR} \geq 1 \quad (11)$$

$$f_c = \frac{(1 - \text{BW})}{2}, 0.5 < \text{OSR} < 1 \quad (12)$$

At certain parameters, the LMS step size versus OSR is eligible for fitting. From Fig. 7b, the step size $\mu$ is quite accurately in-line with the approximation in (13) for OSR $\geq 1$. From (2), (3), (11) and (13), the linearized relationship between $f_c$ and $\mu$ can be deducted, see (14).

$$\mu \approx 0.188/\text{OSR}, \text{OSR} \geq 1 \quad (13)$$

$$\Rightarrow \mu \approx 0.75 \cdot f_c, \text{OSR} \geq 1 \quad (14)$$

For $0.5 < \text{OSR} \leq 1$, the optimization will get more challenging and a polynomial dependencies between $\mu$, $f_c$ and OSR are harder to obtain. For $\text{OSR} \in [0.6, 1]$ we can fit a coarse approximation (15), but as we approach the OSR=0.5 boundary, $\mu$ will increase rapidly.

It should be emphasized here, that the sweeps performed for this work included fixed fractional delay filters and two lowpass filter prototypes. For a specific environment with fixed sps and N, more alternatives should be studied.

$$\mu \approx -0.18 \cdot \text{OSR} + 0.26, \text{OSR} \in [0.6, 1] \quad (15)$$

The practical limits for minimum OSR and maximum bandwidth versus number of paths N are presented in (17) and (18), respectively. These limits are the based on the behavioral simulations. Equation (17) clearly states that the calibration method presented here is limited to OSR > 0.5 (Sub-ADC oversampling ratio). At OSR=0.5, the aliased fundamental fills the frequency band at $f_s/N$.

$$\text{sps} \geq \left\lceil \frac{(N)}{2} \right\rceil + 1 \quad (16)$$

$$\Rightarrow \text{OSR} \geq \frac{\left\lceil \frac{(N)}{2} \right\rceil + 1}{N} \quad (17)$$

$$\Rightarrow \text{BW} \leq N \cdot \frac{f_s}{2 \cdot \left(\left\lceil \frac{(N)}{2} \right\rceil + 1\right)} \quad (18)$$
C. Optimization Sweep Results

In Fig. 8, the number of paths and sps are swept and the timing error related distortion power is shown as a contour plot. Here, the aperture delay mismatches are randomly generated with fixed standard deviation of $0.1/f_s$.

Without calibration, the distortion power contours are shown in Fig. 8a. Considering quantizer noise floor of around 73 dB (12-bit TI-ADC), the noise power due to timing mismatch dominates. In Fig. 8b, the calibration has been used but without any additional lowpass filtering. The calibrations seem to work selectively: the noise floor drops for OSR ≤ 2.

Note that the results in Fig. 8a and 8b do not include quantizer noise but only the distortion power from the timing errors.

In Fig. 9, the additional filtering is active. In cases of FIR and IIR filtering, the calibration is able to find the timing mismatches quite accurately. As shown in Fig. 9 (both subplots), the timing error induced noise floor is below the quantizer noise floor (around 73 dB for 12-bit TI-ADC).

At various OSRs swept, the convergence varied roughly from a few ksamples to a 20 ksamples. As mentioned in Subsection IV-B, many cases are covered ($N$ and sps were swept). For a fixed environment, optimization e.g. choosing filter prototypes should be more thorough to improve convergence.

Figure 10 represents an example with extremely low oversampling ratio (OSR=5/9). Figures 10a and 10b demonstrate a 12-bit 9-path TI-ADC system with 17-tone stimulus at OSR of 5/9: before and after correction. The corrected timing errors reduce the distortion power for about 35 dB. The error convergence for the case of Fig. 10b is depicted in Fig. 10c. The array of estimated errors were obtained at around 16 ksamples (at $f_s/N$, N=9).

From the sweeps, it can be concluded that the suggested timing error calibration method is successful on measuring timing errors even at OSR ≤ 1. As the focus here was on calibration, a few remarks on actual correction should be discussed.

If the corrected TI-ADC output is taken from the output of the Correct block in Fig. 3, the fractional delay filter properties may have a negative impact on the performance. As mentioned, the fractional delay filters are typically the least ideal at half the sampling rate ($f_s/(2N)$). This drawback...
was here avoided by the LMS error reference design (Subsect. IV-B). However, if the corrected TI-ADC output \( u_C(n) \) (Fig. 3) is combined into rate \((f_s)\), the output may contain elevated spurious tone levels for \( {\text{OSR}} \leq 1 \).

In the performance sweeps presented above, the fractional delay filter imperfections were omitted by subtracting the calibrated timing mismatch estimate from the real timing error i.e. the fractional delay filters did not affect the performance results.

In the case of a quadrature baseband TI-ADC, the fractional delay filter related distortion would be avoided by converting LMS taps for in-phase (I) and quadrature-phase (Q) into a correcting I/Q phase shift.

V. GAIN AND OFFSET ERROR CALIBRATION

The timing error calibration scheme is insensitive to gain and offset errors. This means that the calibration of such errors can be done in parallel with aperture delay calibration. Even though the main focus in this work is on timing mismatch calibration, the gain and offset errors are addressed in this Section.

The LMS block for the gain error calibration is shown in Fig. 11, where the error data is the subtraction between \( y_N(n) \) and \( y_k(n) \). The taps in Fig. 11 correspond to negative gain errors \( \hat{e}_{\text{gain}} \), which will be added to cancel the actual gain error in the main model shown in Fig. 12. Here, gain error \( e_{\text{gain}} \) is modeled as in (19).

\[
 u_k(n) \rightarrow u_k(n) \cdot (1 + e_{\text{gain}_k}) \quad (19)
\]

The Sync block in Fig. 12 is copied from Fig. 4. As in the timing mismatch calibration, the zero-error reference in the gain error calibration will be taken from the \( N^{th} \) path. This means that the all calibrated gain errors will contain a bias error not contributing to spurious tones. An example of gain error calibration is shown in Fig. 13, where the parameter \( \mu \) and the lowpass filter are copied from the example in Fig. 10.

In Fig. 13a, gain error with standard deviation of 4 LSB results in the characteristic spur clusters. In Fig. 13b, the gain calibration has been applied resulting in around 4dB reduction in spurious levels. Most likely, this result can be improved with further optimization, but it was not performed in this study.

Digital offset error calibration is not covered in this study. In [6], Law et al. proposed a simple solution of digitizing and subtracting the mean of each channel.

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VII. CONCLUSION

Fully digital timing error calibration scheme for time-interleaved ADC was proposed. The proposed method is capable of compensating the path-related timing errors below Nyquist rate of the individual ADC. Moreover, the proposed method is applicable to any number of paths.

The proposed calibration structure involves both fixed and variable fractional delay filtering along with an adaptive LMS filtering. The aperture delay mismatch estimation by LMS involves filtering that makes calibration insensitive to offset error and gain error along with the imperfections of fractional delay filtering.

This work also presented a gain error calibration method that can be constructed with minor changes to the proposed timing error mismatch calibration structure.

For a given TI-ADC environment and stimulus, the timing mismatch calibration requires optimization that can be performed using a behavioral model. This work covered behavioral simulations with various number of paths and oversampling ratios, so the depth of optimization options was kept normative but minimal. Nevertheless, the calibration was found effective in all the test cases swept.
REFERENCES


