Adaptive Subcarrier, Parameter, and Power Allocation for Partitioned Edge Learning Over Broadband Channels

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Abstract—In this paper, we consider partitioned edge learning (PARTEL), which implements parameter-server training, a well-known distributed learning method, in a wireless network. Thereby, PARTEL leverages distributed computation resources at edge devices to train a large-scale artificial intelligence (AI) model by dynamically partitioning the model into parametric blocks for separated updating at devices. Targeting broadband channels, we consider the joint control of parameter allocation, sub-channel allocation, and transmission power to improve the performance of PARTEL. Specifically, the policies for joint Subcarrier, Parameter, and POWER allocation (SUPPORT) are optimized under the criterion of minimum learning latency. Two cases are considered. First, for the case of decomposable models (e.g., logistic regression), the latency-minimization problem is a mixed-integer program and non-convex. Due to its intractability, we develop a practical solution by integer relaxation and transforming it into an equivalent convex problem of model size maximization under a latency constraint. Thereby, a low-complexity algorithm is designed to compute the SUPPORT policy. Second, consider the case of deep neural network (DNN) models which can be trained using PARTEL by introducing some auxiliary variables. This, however, introduces constraints on model partitioning reducing the granularity of parameter allocation. The preceding policy is extended to DNN models by applying the proposed techniques of load rounding and proportional adjustment to rein in latency expansion caused by the load granularity constraints.

I. INTRODUCTION

Edge machine learning is an area concerning the deployment of learning algorithms at the network edge to gain low-latency access to data and computation resources distributed at a large number of edge devices [1]. In this work, we study the efficient implementation of the well-known method of parameter-server training [2] in a broadband system (e.g., 3GPP 5G) to exploit distributed computation resources at many devices to scale up model training. To this end, several key operations, namely computation-load allocation (via model partitioning), sub-channel allocation, and power control, are jointly designed under the criterion of minimum learning latency.

Two main methods for distributed learning are federated learning [3], [4] and parameter-server training [2], [5], [6], which are designed for different scenarios and features. The key feature of federated learning is its preservation of data privacy. Based on distributed implementation of stochastic gradient descent (SGD), federated learning iterates the separate training of a downloaded model at multiple devices using their local data, and the uploading and aggregation of local models (or local stochastic gradients) to yield a more accurate global model [3], [4]. The avoidance of direct data uploading protects their privacy. Though it is similar to federated learning in implementing distributed SGD, the parameter-server training, which is of our interest, has one distinction. Its purpose is to scale up learning using many resource-constrained machines in a closed network where data privacy is not a concern [2]. To this end, the model is partitioned to allow each device to train only a part of the model instead of the whole as in federated learning. This overcomes the resource constraints of devices and reduces their energy consumption. Moreover, training data are downloaded from a server to devices at the beginning of each round, avoiding their need of persistent storage space.

A current main theme in the field of edge learning is the design of wireless techniques to support efficient deployment of federated learning, resulting in an area called federated edge learning (FEEL) [7]. The effort on overcoming the communication bottleneck of FEEL has led to the design of a new class of multi-access techniques realizing over-the-air model aggregation [8]–[12] and radio resource management (RRM) techniques [13]–[17]. Moreover, researchers have designed energy efficient RRM techniques to tackle the challenge of executing a complex learning task at energy constrained devices in a FEEL system [18]–[20]. Recently, researchers have also explored the efficient implementation of parameter-server training over wireless channels, resulting in a framework called partitioned edge learning (PARTEL) [21]. Let parameter allocation refers to the system operation that to balance computation loads, the server divides the model into parametric blocks of variable lengths and allocate them to devices for separate training. To reduce the learning latency, the technique of joint parameter allocation and resource allocation is proposed in [21], which jointly adapts parameter and bandwidth allocation to devices’ channel states and computation capacities. For simplicity, the prior work assumes narrowband channels, for which the management of uplink radio resource reduces to bandwidth allocation. In this work, we design low-latency PARTEL techniques for a broadband system (e.g., 3GPP 5G) with frequency selective channels. In this case, the frequency resource is managed via sub-channel allocation, which is much more complex than bandwidth allocation. The complexity arises from the fact that the sub-channels of each device have different gains.
and devices have different channel realizations. Consequently, even if the allocated bandwidths are fixed, reshuffling the assignments of sub-channels varies devices’ communication rates. Therefore, jointly designing sub-channel and parameter allocation poses a new challenge that cannot be tackled using the solution in [21].

The optimal sub-channel allocation is well known to be an integer optimization problem that is NP hard [22]–[24]. For the conventional multiuser communication systems, the classic approximate-solution approach has been established in a series of work for the purpose of minimizing sum power under users’ rate constraints [22]–[24]. The essential idea is to relax the integer program and discover the embedded convexity in the relaxed problem to design a practical algorithm [22].

In this work, we build on the classic approach to develop a new solution for the problem of latency minimization in a broadband PARTEL system. The distinction of the current work is the pursuit of a communication-learning integration approach so as to minimize learning latency in the context of PARTEL. To this end, we jointly design parameter and sub-channel allocations. The considerations of synchronized updates by devices, which is a requirement for parameter-server training [2], and devices’ heterogeneous computation capacities introduce more challenges. Existing designs that aim at generic radio access cannot tackle the new challenges, which motivate the current work.

It is also worth mentioning that we also consider a more complex model based on a deep neural network (DNN) besides the decomposable model as in [21]. Unlike the latter, the former is not directly decomposable and requires the modification of learning algorithm to support PARTEL. This introduces additional complexity to the current design.

By tackling the above challenges, we design a set of algorithms for joint Subcarrier, Parameter, Power allocation (SUPPORT), termed joint SUPPORT. The main contributions of this work are summarized as follows.

- **Joint SUPPORT for Decomposable Models**: Consider the case of a decomposable model. The problem of latency minimization by joint SUPPORT is an integer program and intractable. A practical solution approach is developed using two techniques. The first is a relaxation of binary subcarrier assignment decisions. The second is the transformation of the relaxed problem into a convex problem of model size maximization under a latency constraint, which is nested in a simple search for the target model size. Considering the convex problem, the properties of three optimal resource-management operations are analyzed and then applied to design an efficient algorithm for computing the desired SUPPORT policy. Via analysis, it is found that the optimal number of parameters assigned to a worker for updating avoids high power consumption due to overloading. For this reason, the optimal number is derived to be a concave function of its speed and a monotonic decreasing function of its computation power factor. On the other hand, the optimal subcarrier assignment and power allocation over assigned subcarriers are found to favor high channel gains.

- **Joint SUPPORT for DNN Models**: Consider the case of a DNN model. The optimization problems for joint SUPPORT in both mini-rounds are shown to have the same form as that in the preceding case except for additional load granularity constraints. This allows the extension of the joint SUPPORT policy for the case of decomposable models to the current case by rounding down the obtained loads to meet the granularity constraints. Furthermore, the remaining parameters due to rounding are allocated over devices and subcarriers proportionally with their rounded loads, thereby reining in the latency expansion caused by the additional constraints.

The performance gain of the above algorithms and the findings are corroborated using experiments with a real dataset.

The reminder of the paper is organized as follows. In Section II, the system model is introduced. In Section III, the total learning latency minimization problem is formulated. In Section IV and V, the joint SUPPORT designs are proposed for decomposable models and DNN models, respectively. Section VI presents the experimental results followed by concluding remarks in Section VII.

II. MODELS AND METRICS

A. System Model

A single cell OFDM system is considered, as illustrated in Fig. 1(a). In the cell, there are one server equipped with a
single-antenna access point (AP) and K single-antenna edge devices, serving as workers. Each worker performs one task assigned by the server. The server is connected to workers via wireless links. The system bandwidth is divided into N orthogonal subcarriers. The bandwidth of each subcarrier is denoted as B. The frequency-selective fading channel is considered, where different subcarriers will experience different channel gains. We assume that the AP has the channel state information (CSI) of all links that are useful for subcarrier allocation. Besides, the channel gains are assumed to be static in one training iteration but vary over different iterations. The uplink channel gain of worker k on the subcarrier n is denoted as $h_{k,n}$. We denote $\{C_{k,n}\}$ as the subcarrier allocation indicators. If the n-th subscriber is allocated to worker k, then $C_{k,n} = 1$. Otherwise, $C_{k,n} = 0$.

B. Learning Models

1) Decomposable Models: The large-scale learning tasks with decomposable objective functions (such as logistic regression) can be directly implemented using PARTEL based on the method of block coordinate descent. According to the literature (e.g., [21]), a decomposable objective function can be written as

$$L(w) = J(w) + R(w), \tag{1}$$

where $w = \{w_1, w_2, ..., w_L\}^T$ is the parameter vector of the learning model, $L$ is the size of $w$, $J(w)$ is the loss function, and $R(w)$ is the regularized function (e.g., $L_1$ regularization used to increase sparsity and $L_2$ regularization used to reduce overfitting). Specifically, the loss function can be written as $J(w) = \frac{1}{M} \sum_{m=1}^{M} |y_m - \phi(w; x_m)|^2$, where $M$ is the size of the dataset, $\{x_m, y_m\}$ is the m-th data sample, and $\phi(w; x_m)$ is a smooth inference function. The regularized function is a block-separable function, given as $R(w) = \sum_{i=1}^{L} \psi_i(w_i)$, where $w_i$ is an $i$-th element of $w$ and $\psi_i$ is a norm (e.g., $L_1$ or $L_2$ norm). During the training, the smoothness of $\psi_i$ decides the method to update the learning model. If $\psi_i$ is smooth, gradient descent algorithm is used. Otherwise, another method called proximal gradient descent is used.

2) DNN Models: DNN models cannot be directly implemented using PARTEL, as the nested layers therein make the gradient elements of different layers dependent. To make PARTEL feasible and efficient for DNNs, in the sequel, the method of auxiliary variables is used to decompose the DNN models into many independent subproblems [5], [6].

First, consider a DNN model with $G$ hidden layers. The model parameter matrix is denoted as $W$ with the size of $L$ parameters. For an arbitrary layer therein, say layer $g$, the parameter matrix is denoted as $W_g$, the number of neurons is denoted as $L_g$, and the $i$-th neuron parametric vector is denoted as $w_{g,i}$. Therefore, the objective function of the DNN model is given by

$$L(W) = \sum_{m=1}^{M} |y_m - J(x_m; W)|^2, \tag{2}$$

with $J(x; W) = f_{G+1}(... f_1(f_i(x; W_i); W_2); W_3; ...; W_G)$, where the model parameter matrix can be expressed as $W = [W_1, W_2, ..., W_G, W_{G+1}]$, the parameter matrix of the $g$-th layer can be expressed as $W_g = [w_{g,1}, w_{g,2}, ..., w_{g,L_g}]$, and $f_i(x; W_i)$ is the set of output (activation) functions of the $g$-th layer.

**Auxiliary Variables:** The method of auxiliary variables is used by introducing one auxiliary variable per neuron per data sample: $z_{g,i,m} = f(w_{g,i}; z_{g-1,m})$, where $f(\cdot)$ is the activation function, $w_{g,i}$ is the $i$-th neuron parametric vector in layer $g$, $z_{g-1,m}$ is the auxiliary variable introduced for the $i$-th neuron in layer $g$ regarding data sample $m$, $z_{g-1,m} = [z_{g-1,1,m}, z_{g-1,2,m}, ..., z_{g-1,l_{g-1},m}]^T$ is the auxiliary variable vector for the layer $(g-1)$ regarding data sample $m$, and $l_{g-1}$ is the number of neurons in the $(g-1)$-th layer. For an arbitrary data sample, say the $m$-th, the corresponding auxiliary matrix for the whole model is denoted as $Z_m = [z_{1,m}, ..., z_{G,m}, ..., z_{G,m}]$, called per-sample auxiliary matrix. Then the overall auxiliary matrix for all samples are denoted as $Z = [Z_1, ..., Z_{N_m}, ..., Z_M]$.

**Decomposed Optimization:** Following [5], [6], by using the quadratic-penalty method, the problem in (2) is equivalent to minimizing

$$L_Q(W; Z; \mu) = \sum_{m=1}^{M} |y_m - f_{G+1}(z_{G,m}; W_{G+1})|^2 + \mu \sum_{g=1}^{G} \sum_{m=1}^{M} ||z_{g,m} - f_g(z_{g-1,m}; W_g)||^2, \tag{3}$$

where $z_{g,m} = x_m$ and $\mu \rightarrow +\infty$. In (3), the nested structure among layers is decoupled. Consequently, the gradients of any two parameters (or auxiliary variables) are independent.

Finally, the problem in (3) can be solved using the alternating optimization over $W$ and $Z$, i.e., sequentially solving the $W$-stage and $Z$-stage, defined below, in each training iteration.

- **W-stage:** Fixing the values of $Z$, solve the problem of

$$\min_{W,g,i} L_Q(W; Z; \mu), \tag{4}$$

where $w_{g,i}$ and $z_{g,i,m}$ are the parameteric vector and auxiliary variable of the $i$-th neuron in the $g$-th layer, respectively, $z_{g-1,m}$ is the auxiliary variable vector of the $(g-1)$-th layer. Note that one device is allocated a task of updating one or more neuron parametric vectors by solving the subproblems in (4).

- **Z-stage:** Conditioned on the values of $W$, solve the problem of

$$\min_{Z,n} L_Q(W; Z; \mu), \tag{5}$$

where $Z_m$ is the per-sample auxiliary matrix corresponding to data sample $m$. The size of the per-sample auxiliary matrix is

$$\sum_{g=1}^{G} I_{g}$$

with $I_{g}$ being the number of neurons in layer $g$. Note that one device is allocated a task of updating one or more per-sample auxiliary matrices by solving the subproblems in (5).
C. PARTEL Architecture

Consider the PARTEL system and operations in Fig. 1, that are elaborated as follows.

1) Decomposable Models: The model-parameter vector is partitioned into K disjoint parametric blocks, as \( w = \{w_1, ..., w_k, ..., w_K\} \), where \( w_k \) is allocated to worker \( k \) for update, using a downloaded global dataset from the server. The communication overhead for the server to broadcast the dataset is ignored, as its large power and bandwidth are used and the dataset broadcasting requires only once before the model training. One main benefit of PARTEL is low learning latency, as each resource-constrained worker is required to calculate and transmit the gradient or proximal gradient of only a parametric block instead of the whole parameter vector during each iteration [21].

In the PARTEL framework, one training iteration of the decomposable models is called one (communication) round. As shown in Fig. 1(b), there are three phases in each round, as follows.

- **Push Phase**: The server broadcasts the whole model-parameter vector, \( w \), to all workers.
- **Computation Phase**: Each worker computes the update (e.g., gradients or proximal gradients) of its allocated parametric block.
- **Pull Phase**: All workers upload the updates of their corresponding parametric blocks to the server. The server updates the whole parameter vector.

The training process in Fig. 1(b) iterates when all parametric blocks are updated in the round, i.e., the tasks of all workers are synchronized in each round.

2) DNN Models: As mentioned, each round of DNN models comprises two stages: W-stage and Z-stage, described as follows.

- **W-stage**: The parameter matrix \( W \) is divided into \( K \) blocks, with each being updated by one worker. To avoid inter-communication among different workers, the following load-granularity constraint is applied.

  **Granularity Constraint 1 (Neuron Allocation for W-stage)**. In W-stage, each neuron parametric vector (e.g., \( w_{f,g,b} \)) defined in (4) should be allocated to one and only one worker.

- **Z-stage**: The auxiliary matrix \( Z \) is divided into \( K \) blocks, with each being updated by one worker. To avoid inter-communication among workers, another load-granularity constraint is applied.

  **Granularity Constraint 2 (Per-Sample Auxiliary Matrix Allocation for Z-stage)**. In Z-stage, each per-sample auxiliary matrix (e.g., \( Z_{m,n} \)) defined in (5) should be allocated to one and only one worker.

**Example 1.** Since the number of neurons in a DNN model and the data samples used for training are large, the sizes of each neuron problem and each per-sample auxiliary matrix problem are relatively small, compared with the whole learning tasks, making the model partitioning meaningful. As an example, our experiments involve the DNN model “Lenet-5” proposed in [25] trained on the MNIST dataset. A mini batch of 50 samples is used in each training iteration. In “Lenet-5”, there are 3 convolutional layers, including 142 feature maps in total. The first two convolutional layers are followed by a pooling layer and the last is followed by a fully connected layer with 84 neurons. In \( W \)-stage, the number of independent subproblems is \( I = 142 + 84 = 226 \). The size of each neuron problem is about \( \frac{1}{7} = \frac{1}{226} \) of the whole problem. In Z-stage, the size of each per-sample auxiliary matrix problem is \( \frac{1}{50} \) of the whole problem.

Each stage (W-stage or Z-stage) comprises three phases, push, computation, and pull, which are similar to those in the case of decomposable models. The main difference lies in the additional Granularity Constraint 1 or 2. Each round comprises two stages and the rounds are repeated until the DNN model converges.

D. Latency and Energy Consumption Models

Consider an arbitrary communication round and an arbitrary worker, say worker \( k \). The latency and energy consumption models of each phase are described as below.

1) **Push Phase**: The push latency is the time for the server to broadcast the whole model-parameter vector to all workers. It is a constant identical for all workers. Besides, as the transmit power and bandwidth are very large during broadcasting, the push latency can be ignored. In this step, the energy consumption by all workers is to receive the model-parameter vector from the server and is included in the circuit energy consumption, denoted as \( \xi \).

2) **Computation Phase**: The computation latency of worker \( k \) depends on the size of the allocated parametric block \( L_k \) and its computation speed \( f_k \): \n
\[
T_{k,cmp} = \frac{L_k}{f_k}, \quad 1 \leq k \leq K, \tag{6}
\]

where \( f_k \) is measured by the number of parameters processed per second.

According to [26], the computation power of worker \( k \) is \( P_{k,cmp} = g_k f_k^3 \), where \( g_k \) is the computation power factor. Then, the computation energy of worker \( k \) is \n
\[
E_{k,cmp} = P_{k,cmp} \times T_{k,cmp} = g_k f_k^3 L_k, \quad 1 \leq k \leq K. \tag{7}
\]

3) **Pull Phase**: The pull phase consists of two parts. One is uploading gradient blocks from workers to the server. The other is the server updating the global model using the gradients sent by the workers. For the latter part, there is no energy consumption at the workers. Its latency, denoted as \( T_r \), is a constant and is same for all workers. In the sequel, we ignore the model update latency, \( T_r \), as it is small and has no impact on the solution of latency minimization.

For uploading, worker \( k \) transmits over a set of assigned subcarriers. We denote \( T_{k,trans} \) as the uploading latency of worker...
k on subcarrier n. If subcarrier n is not allocated to k, i.e., $C_{k,n} = 0$, $T_{k,n}^{com} = 0$. Otherwise,
\[
T_{k,n}^{com} = \frac{L_{k,n}}{R_{k,n}}, \quad \forall C_{k,n} = 1, \tag{8}
\]
where $L_{k,n}$ is the number of parameters uploaded by worker k on subcarrier n, $\tau$ is the number of bits per gradient element, and $R_{k,n}$ is the channel capacity of worker k on subcarrier n. The channel capacity is given by $\{R_{k,n} = B \log_2 (1 + P_{com}^{k,n} h_{k,n} / \sigma^2), \forall (k,n)\}$, where $B$ is the subcarrier bandwidth, $\sigma^2$ is the power of additive white Gaussian noise, $P_{com}^{k,n}$ is the transmit power, and $h_{k,n}$ is the channel gain of worker k on subcarrier n, respectively. It follows that
\[
P_{com}^{k,n} = \frac{(\rho/h_{k,n}^2 - 1) \sigma^2}{h_{k,n}}, \quad \forall (k,n). \tag{9}
\]
Then, the overall uploading latency of worker k is decided by the slowest subcarrier:
\[
T_k^{com} = \max_n T_{k,n}^{com}, \quad 1 \leq k \leq K. \tag{10}
\]

The uploading energy consumption of worker k is modeled as follows. Let $E_{com}^{k,n}$ denote the transmit energy consumption of worker k on subcarrier n. If subcarrier n is not allocated, i.e., $C_{k,n} = 0$, $E_{com}^{k,n} = 0$. Otherwise,
\[
E_{com}^{k,n} = C_{k,n} P_{com}^{k,n} T_{k,n}^{com}, \quad \forall (k,n). \tag{11}
\]
By substituting the transmit power density $P_{com}^{k,n}$ in (9) and the uploading latency $T_{k,n}^{com}$ in (8), $E_{com}^{k,n}$ can be further derived as
\[
E_{com}^{k,n} = C_{k,n} \left(\frac{\rho h_{k,n} / B - 1}{h_{k,n} R_{k,n}}\right) \sigma^2 L_{k,n} \tau, \quad \forall (k,n). \tag{12}
\]
The total uploading energy consumption of worker k is the sum of uploading energy consumption over all subcarriers:
\[
E_{com}^k = \sum_{n=1}^{N} E_{com}^{k,n}, \quad 1 \leq k \leq K. \tag{13}
\]

Next, the total latency and energy consumption of worker k are defined as follows. The latency of worker k is the sum latencies of the two phases:
\[
T_k = T_k^{com} + T_k^{cal}, \quad 1 \leq k \leq K, \tag{14}
\]
where $T_k^{cal}$ is the computation latency defined in (6), $T_k^{com}$ is the uploading latency defined in (10). The energy consumption of worker k is given by:
\[
E_k = E_k^{com} + E_k^{cal} + \xi, \quad 1 \leq k \leq K, \tag{15}
\]
where $\xi$ is the constant circuit energy consumption when there is no computation and transmission, $E_k^{cal}$ defined in (7) and $E_k^{com}$ defined in (13) are the computation and uploading energy consumption of worker k, respectively.

III. PROBLEM FORMULATION

We aim at minimizing the overall learning latency of the PARTEL system, which depends on two factors: the per-round latency and the number of rounds for model convergence. The overall learning latency is defined as the total latency of all rounds till model convergence. In [21] for narrowband channels, it is proved that the overall learning latency minimization is equivalent to separately minimizing the per-round latency. The result can also apply to the current case of broadband channels, as stated below.

**Lemma 1 (Equivalent Per-Round Latency Minimization [21]).**
The overall learning latency minimization is equivalent to separately minimizing the latencies for all rounds.

Lemma 1 holds because the distributed learning algorithms implemented using PARTEL are equivalent to the corresponding centralized ones in terms of convergence rate as measured by the required number of communication rounds. Specifically, for distributed learning, the values of updates (e.g., gradients and proximal gradients) calculated in each round and the number of rounds required for model convergence are independent of SUPPORT.

Using the result in Lemma 1, we formulate the equivalent per-round latency-minimization problem. For an arbitrary round, we aim to minimize its latency, denoted as $T$, under the constraints on subcarrier assignment, latency requirement, parameter allocation, and power control, described as follows.

1) **Subcarrier Assignment Constraints:** Each subcarrier can be allocated to one worker:
\[
\left\{ \begin{array}{ll}
C_{k,n} \in \{0, 1\}, & \forall (k,n), \\
\sum_{k=1}^{K} C_{k,n} = 1, & 1 \leq n \leq N,
\end{array} \right. \tag{16}
\]
where $C_{k,n} = 1$ represents that the subcarrier n is allocated to worker k.

2) **Per-Round Latency Constraints:** As all parametric blocks should be updated in one round, all workers’ latencies, say ($T_k$), should not exceed the overall one-round latency $T$:
\[
T_k \leq T, \quad 1 \leq k \leq K. \tag{17}
\]
As mentioned, $T$ is the latency for an arbitrary round and can be different over different rounds. By substituting $T_k$ in (14), the constraints in (17) can be derived as
\[
T_k^{com} + T_k^{cal} \leq T, \quad 1 \leq k \leq K, \tag{18}
\]
which, by substituting the uploading latency $T_k^{com}$ in (10), are equivalent to
\[
\left\{ \begin{array}{ll}
C_{k,n} = 1, & \forall (k,n), \\
T_k^{cal} + T_k^{com} \leq T, & \forall (k,n),
\end{array} \right. \tag{C2}
\]
where $T_k^{cal}$ defined in (6) is the computation latency of worker k and $T_k^{com}$ defined in (8) is the uploading latency of worker k on subcarrier n.
3) Parameter Constraints: The parameter constraints are two tiers. On the one hand, the total updatable number of parameters by all workers should be no smaller than the size of the model:

\[(C3) \sum_{k=1}^{K} L_k \geq L,\]  \hspace{1cm} (20)

where \(L_k\) is the size of the parametric block allocated to worker \(k\) and \(L\) is the size of the model-parameter vector (or matrix). On the other hand, for each worker, the total uploaded number of parameters on all subcarriers should be no smaller than its allocated parametric-block size:

\[(C4) \sum_{n=1}^{N} C_{k,n} L_{k,n} \geq L_k, \quad 1 \leq k \leq K,\] \hspace{1cm} (21)

where \(L_{k,n}\) is the number of parameters uploaded by worker \(k\) on subcarrier \(n\). In the sequel, \(\{L_k\}\) and \(\{L_{k,n}\}\) are relaxed to be continuous for simplicity. In practice, the solved \(\{L_k^*\}\) and \(\{L_{k,n}^*\}\) will be rounded for implementation and the loss caused by the rounding operation can be ignored, since the values of \(\{L_k\}\) and \(\{L_{k,n}\}\) are typically large.

For the case of DNN models, Granularity Constraints 1 and 2 can be written mathematically as follows.

\[(C_{dnn}) \frac{L_k}{L_{sub}} \in \mathbb{N}^+, \quad 1 \leq k \leq K,\] \hspace{1cm} (22)

where \(\mathbb{N}^+\) is the set of positive integers and \(L_{sub}\) is the size of the subproblems, i.e., neurons or per-sample auxiliary matrices. For W-stage, the size of all neurons, say \(L_{sub}\), are assumed the same for simplicity, which has little impact on the solution, since the size of each neuron is much smaller than that of the whole problem, as mentioned in Example 1. For Z-stage, the size of each per-sample auxiliary matrix is the total number of neurons, say \(L_{sub} = \sum_{g=1}^{G} I_g\), with \(I_g\) being the number of neurons in layer \(g\).

4) Power Constraints: The power consumption of each worker is constrained as

\[(C5) \frac{E_k}{T_k} \leq P_k, \quad 1 \leq k \leq K,\] \hspace{1cm} (23)

where \(E_k\) defined in (15), \(T_k\) defined in (14), and \(P_k\) are the energy consumption, latency, and maximal permitted power of worker \(k\), respectively.

5) Latency-Minimization Problem: Under these constraints, the per-round latency-minimization problem by joint SUPPORT can be formulated as

\[\min_{\{C_{k,n}\}, \{L_k\}, \{R_{k,n}\}, \{T\}} T, \quad \text{s.t. } (C1) \sim (C5), \text{ and } (C_{dnn}) \text{ for a DNN Model.} \] \hspace{1cm} (P1)

A. Equivalent Latency Requirement

First, the following necessary condition for the equivalent latency requirement can be derived to simplify Problem (P1). Note that in [21], similar equivalent latency property can be derived in the PARTEL design for frequency non-selective channels. However, for OFDM systems considered in this paper, the binary subcarrier allocation among workers and the corresponding inter-subcarrier power and parameter allocation for each device make the problem much more complicated.

**Lemma 2 (Equivalent Latency for All Workers).** To achieve the optimal solution of (P1), the following latency condition should be satisfied:

\[T_{\text{cmp}}^k + T_{\text{com}}^k = T, \quad \forall C_{k,n} = 1,\] \hspace{1cm} (25)

where \(T_{\text{cmp}}^k\) defined in (6) is the computation latency of worker \(k\), \(T_{\text{com}}^k\) defined in (8) is the uploading latency of worker \(k\) on subcarrier \(n\).

**Proof:** See Appendix A.

The result in Lemma 2 yields the following insights. First, it requires all workers the same latency with the overall latency \(T\). Second, for each worker, the uploading latency on all allocated subcarriers should be equal.

**Remark 1 (Computation Latency vs. Communication Latency).** By substituting the computation latency \(T_{\text{cmp}}^k\) in (6) and the communication latency \(T_{\text{com}}^k\) in (8) into the equivalent latency property in (25), it can be derived as:

\[\frac{L_k}{T_k} + \frac{L_{k,n} \tau}{R_{k,n}} = T, \quad \forall C_{k,n} = 1,\] \hspace{1cm} (26)

with the constraints \(\sum_{n=1}^{N} C_{k,n} L_{k,n} = L_k\). From (26), the load, say \(L_k\), has the same effect on computation and communication latency. On the other hand, when the computation frequency \(f_k\) is small compared to the number of subcarriers and the data rates, the computation latency dominates or vice versa.

By substituting the computation latency \(T_{\text{cmp}}^k\) defined in (6) and the uploading latency \(T_{\text{com}}^k\) defined in (8) into the necessary condition in Lemma 2, we can derive the number of parameters uploaded by worker \(k\) on subcarrier \(n\), say \(L_{k,n}\), as

\[L_{k,n} = \frac{C_{k,n} R_{k,n} \tau}{T - \frac{L_k}{T_k}}, \quad \forall (k,n),\] \hspace{1cm} (27)

where \(T\) is the per-round latency, \(C_{k,n} \in \{0,1\}\) is the subcarrier-allocation indicator, \(L_k\) is the parametric-block size allocated to worker \(k\), \(R_{k,n}\) is the channel capacity of \(k\) on subcarrier \(n\). By substituting \(L_{k,n}\) defined in (27) and the necessary condition in Lemma 2, Problem (P1) can be simplified as:

\[\min_{\{C_{k,n}\}, \{L_k\}} T, \quad \text{s.t. } (C1), (C3), \quad \sum_{n=1}^{N} \frac{C_{k,n} R_{k,n} \tau}{T - \frac{L_k}{T_k}} \geq L_k, \forall k,\] \hspace{1cm} (P2)

\[E_k \leq P_k T, \forall k.\]
where $E_k$ defined in (15) is the energy consumption of worker $k$. By substituting $L_{k,n}$ in (27), $E_k$ can be expressed as

$$E_k = g_k f_k^2 L_k + \sum_{n=1}^{N} C_{k,n} \left( \frac{2^{R_{k,n}/f_k} - 1}{h_{k,n}} \right) \left( T - \frac{L_k}{f_k} \right) + \xi. \quad (29)$$

B. Equivalent Convex Problem

Problem (P2) is a mixed integer non-convex problem and is hence NP-hard [27]. In the sequel, two steps are used to tackle it. First, following the standard approach to tackle integer programming (see e.g., [22]), linear programming relaxation is used to relax the subcarrier-allocation indicators in Problem (P2) to be continuous, i.e., \{C_{k,n} \in [0, 1], \forall (k, n)\}. Then, following the method in [21], the relaxed problem can be equivalently converted to the problem of updatable model size maximization. However, it remains non-convex and difficult to tackle due to the intra-worker parameter constraint and the power constraint. In the sequel, the problem of updatable model size maximization is derived and solved.

Given the one-round latency $T$ for an arbitrary round, let $\hat{L}^*(T)$ denote the maximum size of a model that can be updated within the round. Then $\hat{L}^*(T)$ solves the following problem of model size maximization:

$$\hat{L}^*(T) = \max_{\{C_{k,n} \cdot (\{k\} \cdot (R_{k,n})\}} \sum_{k=1}^{K} L_k,$$

s.t. \(0 \leq C_{k,n} \leq 1, \forall (k, n),\) \(C_{k,n} = 1, \forall n,\) \(\sum_{n=1}^{N} C_{k,n} R_{k,n} \leq L_k, \forall k,\) \(E_k \leq P_k T, \forall k,\)

(P3)

where $C_{k,n}$ is the subcarrier-allocation indicator, $L_k$ is the parametric-block size allocated to worker $k$, $R_{k,n}$ is the channel capacity of worker $k$ on subcarrier $n$, $T$ is the one-round latency, $E_k$ defined in (29) is the energy consumption of worker $k$. Note that solving Problem (P2) via utilizing the problem of model size maximization in Problem (P3) follows the method in [21]. However, new challenges arise from the subcarrier allocation among workers and the inter-subcarrier power and parameter allocation for each worker, leading to the non-convexity and a much larger size of Problem (P3).

**Lemma 3 (Relation of Maximal Model Size and Latency).** $\hat{L}^*(T)$ defined in Problem (P3) is a monotonously increasing function of $T$.

**Proof:** See Appendix B.

It follows from the result in Lemma 3 that the solution of Problem (P2) is the minimal latency, say $T^*$, which makes the updatable model size $\hat{L}^*(T^*)$ no less than the target size $L$. This suggests a method to solve Problem (P2) by searching $T^*$ using the criterion $\hat{L}^*(T) \geq L$, which will be elaborated in the later subsection.

To get the maximum updatable model size $\hat{L}^*(T)$ requires solving Problem (P3). To this end, the following variables are used to transform Problem (P3) into a convex problem.

$$\begin{align*}
\varphi_k &= \left( T - \frac{L_k}{f_k} \right)^{-1}, \\
\tilde{R}_{k,n} &= C_{k,n} R_{k,n},
\end{align*} \quad (31)$$

By substituting the variables in (31) and $E_k$ defined in (29), Problem (P3) can be written as

$$\begin{align*}
\hat{L}^*(T) &= \max_{\{C_{k,n} \cdot (\{k\} \cdot (R_{k,n})\}} \sum_{k=1}^{K} f_k \left( T - \frac{1}{\varphi_k} \right), \\
s.t. \(0 \leq C_{k,n} \leq 1, \forall (k, n),\) \(C_{k,n} = 1, \forall n,\) \(\sum_{n=1}^{N} \tilde{R}_{k,n} \geq f_k (T \varphi_k - 1), \forall k,\) \(D_k \leq (P_k T - \xi) \varphi_k, \forall k,\)
\end{align*} \quad (P4)$$

where

$$D_k = \sum_{n=1}^{N} C_{k,n} \sigma^2 \left( \frac{2^{R_{k,n}/f_k} - 1}{h_{k,n}} \right) + g_k f_k^2 (\varphi_k T - 1). \quad (33)$$

**Lemma 4.** Problem (P4) is a convex problem.

**Proof:** See Appendix C.

C. Properties of Optimal Policies

Based on the results in the previous subsection, the optimal policies of Problem (P2) with relaxed subcarrier-allocation indicators are proposed, as described in the following.

As (P4) is convex, the primal-dual method can be used to get the optimal solution:

$$\max_{\{\mu_k \cdot (\{k\} \cdot (\nu_k))\}} \min_{\{\nu_k\}} \mathcal{L}_{\mathcal{P}_k},$$

where $\mathcal{L}_{\mathcal{P}_k}$ is the Lagrange function of Problem (P4), given as

$$\mathcal{L}_{\mathcal{P}_k} = - \sum_{k=1}^{K} f_k \left( T - \frac{1}{\varphi_k} \right) + \sum_{n=1}^{N} \mu_n \left( 1 - \sum_{k=1}^{K} C_{k,n} \right) + \sum_{k=1}^{K} \mu_k \left( f_k (T \varphi_k - 1) - \sum_{n=1}^{N} \tilde{R}_{k,n} \right) \quad (35)$$

$$+ \sum_{k=1}^{K} \nu_k \left[ D_k - (P_k T - \xi) \varphi_k \right],$$

$D_k$ is defined in (33), and \{\mu_k\}, \{\lambda_k \geq 0\}, and \{\nu_k \geq 0\} are Lagrangian multipliers.

Next, the necessary conditions for achieving the optimal solution of the inner loop are used to derive the optimal policies. The inner loop problem is given by

$$\min_{\{C_{k,n} \cdot (\{k\} \cdot (\nu_k))\}} \mathcal{L}_{\mathcal{P}_k}, \quad \text{given } \{\mu_k\}, \{\lambda_k \}, \{\nu_k\},$$

where

$$\frac{\partial \mathcal{L}_{\mathcal{P}_k}}{\partial R_{k,n}} = - \frac{\lambda_k + \nu_k 2^{R_{k,n}/f_k} \ln 2 \cdot \sigma^2}{h_{k,n}} = 0, \quad \forall C_{k,n} \neq 0, \quad (37)$$

The first necessary condition is
which gives the following optimal scheme for calculating the channel capacity:

\[
R_{k,n}^* = \begin{cases} 
\frac{\tilde{R}_k}{C_{k,n}^*}, & \forall C_{k,n} \neq 0, \\
0, & \text{otherwise}, 
\end{cases} 
\tag{38}
\]

where

\[
\frac{\tilde{R}_k}{C_{k,n}^*} = B \log_2 \left( \frac{\lambda_k B}{\nu_k \tau \ln 2} \right) + B \log_2 \left( \frac{h_{k,n}}{\sigma^2} \right). 
\tag{39}
\]

By substituting \(R_{k,n}^*\) in (38) into the transmission power in (9), the optimal power-allocation scheme can be derived, as in the following lemma.

**Lemma 5 (Optimal Power Allocation).** The optimal power-allocation scheme is

\[
P_{k,n}^{\text{opt}} = \begin{cases} 
\frac{\lambda_k B}{\nu_k \tau \ln 2} - \frac{\sigma^2}{h_{k,n}}, & \forall C_{k,n} \neq 0, \\
0, & \text{otherwise.} 
\end{cases} 
\tag{40}
\]

The water-filling like result in (40) shows that for each worker, more power should be allocated on the subcarrier with high channel gain, say \(h_{k,n}\).

The second necessary condition to achieve the optimum of the inner loop problem in (36) is

\[
\frac{\partial L_{P4}}{\partial \varphi_k} = -\frac{f_k}{\nu_k^2} + \lambda_k f_t T + \nu_k g_k f_k^T - \nu_k (P_k T - \xi) = 0. 
\tag{41}
\]

By substituting the variable transformations in (31) into (41), we can achieve the optimal inter-worker parameter allocation scheme, as follows.

**Lemma 6 (Optimal Parameter Allocation Among Workers).** The optimal inter-worker parameter-allocation scheme is

\[
L_k^* = T - \sqrt{\frac{\lambda_k T + \nu_k g_k f_k^2 T - \nu_k (P_k T - \xi)}{f_k}}, \quad \forall k. 
\tag{42}
\]

In (42), the optimal parametric-block size allocated to worker \(k\), say \(L_k^*\), is a concave function of the computation speed \(f_k\) and a monotonically decreasing function of the computation power factor \(g_k\). On one hand, large \(f_k\) can reduce the computation latency. On the other hand, the computation energy increases as a square function of \(f_k\). The optimal load in (42) balances the two aspects.

Substituting the parameter-allocation scheme in (42) and the channel capacity in (38) into the intra-worker parameter-allocation scheme \(\{L_{k,n}\}\) in (27), gives the following lemma.

**Lemma 7 (Optimal Parameter Allocation Among Subcarriers).** The optimal intra-worker parameter allocation scheme is given by

\[
L_{k,n}^* = \begin{cases} 
G_k B \log_2 \left( \frac{\lambda_k B h_{k,n}}{\nu_k \tau \ln 2} \right), & \text{if } C_{k,n} \neq 0, \\
0, & \text{otherwise.} 
\end{cases} 
\tag{43}
\]

where

\[
G_k = \sqrt{\frac{\lambda_k T + \nu_k g_k f_k^2 T - \nu_k (P_k T - \xi)}{\tau}}. 
\]

From (43), more parameters should be assigned to the channel with high gain.

The third necessary condition to achieve the optimum of the inner loop problem in (36) is

\[
\frac{\partial L_{P4}}{\partial C_{k,n}} = -\mu_n + I_{k,n} = 0, \quad \forall (k,n), 
\tag{44}
\]

where \(I_{k,n}\) is the indicator function given by

\[
I_{k,n} = \frac{\nu_k \sigma^2}{h_{k,n}} \left( \frac{2 \tilde{R}_{k,n}}{h_{k,n}} - 1 \right) - \frac{\tilde{R}_{k,n} \nu_k^2 \ln 2}{B}, \quad \forall (k,n). 
\tag{45}
\]

Note that \(I_{k,n}\) is determined when \(R_{k,n}^*\) is known. Let \(\mu_n = \min I_{k,n}\). If \(I_{k,n} \geq \mu_n\), \(C_{k,n} = 0\), as the condition in (44) can not be satisfied. If \(I_{k,n} = \mu_n\) for a unique worker, say \(k\), then \(C_{k,n} = 1\). If \(I_{k,n} = \mu_n\) for multiple workers, then \(C_{k,n} \in (0,1)\) for these workers. And in the last case, it is easy to show that the values of the non-zero \(\{C_{k,n}\}\) won’t influence the value of the Lagrange function \(L_P\), defined in (35), as long as the subcarrier assignment constraint, say \(\sum_{n=1}^{N} C_{k,n} = 1, \quad 1 \leq n \leq N\), is satisfied.

The optimal subcarrier allocation is summarized in the following lemma.

**Lemma 8 (Optimal Subcarrier Allocation).** The optimal subcarrier allocation is given as:

\[
C_{k,n}^* = \begin{cases} 
0, & \text{if } I_{k,n} > \mu_n, \\
(0,1), & \text{if } I_{k,n} = \mu_n \text{ for multiple workers}, \\
1, & \text{if } I_{k,n} = \mu_n \text{ for a unique worker} W_k, 
\end{cases} 
\tag{46}
\]

where \(I_{k,n}\) is the indicator function defined in (45), \(\mu_n = \min I_{k,n}\), and \(R_{k,n}^*\) is the optimal channel capacity in (38).

In (46), a high channel gain leads to a small value of \(I_{k,n}\) and thus a high possibility to make \(C_{k,n} \neq 0\). That means the subcarrier with higher channel gain has larger possibility to be allocated to the worker. Note that in the optimal scheme in Lemma 8, some subcarrier-allocation indicators may be fractions. The standard approach is to round those to be binary (see, e.g., [22]), which will be elaborated in the later subsection.

**D. Optimal Policy Computation**

In this subsection, the joint SUPPORT algorithm to solve the original Problem (P1) is proposed. First, we solve the convex Problem (P4) by the primal-dual method using the closed-form results in Lemmas 6-8. Some notation is described as follows. \(\{\nu_k\}\) and \(\{\nu_k\}\) denote the step sizes of gradient descent. \(L_P\) and \(\mu\) \(\{\lambda_k \geq 0\}\), and \(\{\nu_k \geq 0\}\) are the Lagrange function and Lagrangian multipliers defined in (35). With the notation, the application of the primal-dual method yields Algorithm 1 for solving Problem (P4).

**Remark 2 (Low Complexity of Updatable Model Size Maximization).** The computation complexity of Algorithm 1 is \(O(K^2 N^3)\) with \(K\) being the number of workers and \(N\) being the number of subcarriers, as the closed-form results in Lemmas 6 - 8 makes the updating of corresponding variables more efficient. As a comparison, directly solving the non-convex Problem (P3) has a computational complexity of at least \(O(K^2 N^3)\) and is suboptimal.
Algorithm 1: Updatable Model Size Maximization
1: **Input:** channel gains \( \{ h_{k,n} \} \), computation speeds \( \{ f_k \} \), computation power factors, \( \{ g_k \} \), and the given one-round latency \( T \).
2: **Initialize** \( \{ \lambda_k^{(0)} \} \), \( \{ \nu_k^{(0)} \} \), and \( i = 0 \).
3: **Loop**
   4: Update the multipliers as
   \[
   \begin{align*}
   \lambda_k^{(i+1)} &= \max \left\{ \lambda_k^{(i)} + \eta_k \frac{\partial C_k}{\partial \lambda_k}, 0 \right\}, \quad 1 \leq k \leq K, \\
   \nu_k^{(i+1)} &= \max \left\{ \nu_k^{(i)} + \eta_k \frac{\partial C_k}{\partial \nu_k}, 0 \right\}, \quad 1 \leq k \leq K,
   \end{align*}
   \]
5: Solve \( \{ L_k^* \} \), \( \{ R_{k,n}^* \} \), and \( \{ C_{k,n}^* \} \) using (42), (38), and (46), respectively.
   6: Get \( \{ \tilde{\phi}_k \} \) and \( \{ \tilde{R}_{k,n} \} \) with (31).
7: **Until Convergence**
8: \( L^* (T) = \sum_{k=1}^{K} L_k^* \).
9: **Output:** \( L^* (T) \), \( \{ L_k^* \} \), \( \{ R_{k,n}^* \} \), and \( \{ C_{k,n}^* \} \).

Algorithm 2: Joint SUPPORT
1: **Input:** channel gains \( \{ h_{k,n} \} \), computation speeds \( \{ f_k \} \), and computation power factors, \( \{ g_k \} \).
2: **Select** \( T = T_u \) that makes \( L^* (T_u) \) defined in Problem (P4) larger than \( L \).
3: **Select** \( T = T_i \) that makes \( L^* (T_i) < L \).
4: While \( T_u \neq T_i \)
5: Let \( T_m = (T_u + T_i)/2 \).
6: Input \( \{ h_{k,n} \} \), \( \{ f_k \} \), \( \{ g_k \} \) and \( T = T_m \) into Algorithm 1 to solve (P4).
7: Obtain \( L^* (T_m) \), \( \{ L_k^* \} \), \( \{ R_{k,n}^* \} \), and \( \{ C_{k,n}^* \} \).
8: If \( L^* (T_m) \geq L \)
   9: \( T = T_m \).
Else
   10: \( T = T_i \).
11: End if
12: End while
13: **Output:** \( T^* \), \( \{ L_k^* \} \), \( \{ R_{k,n}^* \} \), and \( \{ C_{k,n}^* \} \).

Then, as mentioned in the preceding subsection, Problem (P2) with relaxed subcarrier-allocation indicators can be solved by nesting a one-dimensional search over the latency \( T \) and solving the convex Problem (P4). Based on the monotonicity of \( L^* (T) \) in Lemma 3, the search can be efficiently implemented by bisection method. While the solution of Problem (P4) is presented in Algorithm 1. Then the optimal policy to solve Problem (P2) with relaxed subcarrier-allocation indicators is presented in Algorithm 2, by nesting the bisection search and Algorithm 1.

Finally, based on Algorithm 2, the joint scheme of SUPPORT without relaxation is proposed to solve the original Problem (P1). Note that not all subcarrier-allocation indicators solved by Algorithm 2 are integers, i.e., \( C_{k,n}^* \in (0, 1) \) for some \((k, n)\). For these subcarriers, a practical subcarrier-allocation scheme following [22] is determined as

\[
C_{k_1,n}^* = 1, \quad k_1 = \arg \max_{k} L_k^*, \quad 1 \leq n \leq N, \tag{47}
\]

where the subcarrier is allocated to the worker with the largest value. Then, given the subcarrier-allocation scheme \( \{ C_{k,n}^* \} \), the latency-minimization problem is a special case of Problem (P1), whose solution can also be solved by Algorithm 2.

V. JOINT SUPPORT FOR DNN MODELS

In this section, DNN models are considered. Since Problem (P1) is not tractable in this case with the additional constraint \( (C_{dnn}) \), we propose an approximate solution method that leverages the result for decomposable model case, described as follows.

1) For both W-stage and Z-stage, solve the joint scheme of SUPPORT using the method in Section IV without considering Granularity Constraints 1 and 2.
2) Given the subcarrier-allocation scheme, round the parameter allocation for each worker to satisfy Granularity Constraint 1 for W-stage and Granularity Constraint 2 for Z-stage.

The challenges lie in Step 2) and are two-fold. On one hand, how should the rounding indicator be designed to minimize the rounding loss. On the other hand, as each worker’s number of parameters changes, the corresponding channel-capacity (or power) allocation and intra-worker parameter allocation among the assigned subcarriers should be redesigned. To tackle these challenges, in the sequel, we first propose a joint scheme of SUPPORT for DNN models. Then, the rounding scheme is designed accordingly and the resultant latency increase is analyzed.

1) Joint SUPPORT for DNN Models: Denote the solved one-round latency as \( T^* \), the subcarrier-allocation policy as \( \{ C_{k,n}^* \} \), the spectrum efficiencies as \( \{ R_{k,n}^* \} \), the number of parameters of worker \( k \) as \( L_k^* \), the number of parameters uploaded by worker \( k \) on subcarrier \( n \) as \( L_{k,n}^* \).

Consider an arbitrary worker, say worker \( k \). If its number of parameters is rounded down to satisfy \( (C_{dnn}) \), the reduced number of parameters is denoted as \( \Delta L_{k,n} \geq 0 \). If its number of parameters is rounded up, the additional number of parameters to be uploaded is denoted as \( \Delta L_{k,n} \geq 0 \). Note that if worker \( k \)’s number of parameters is rounded down, no influence is caused to the one-round latency. Hence, only the case of being rounded up is considered in the sequel. Our aim is to design rounding scheme to minimize the resulted additional one-round latency.

Next, the joint scheme of SUPPORT is designed as

\[
\begin{align*}
C_{k,n}^* &= C_{k,n}^*, \\
R_{k,n}^* &= R_{k,n}^*, \\
\Delta L_{k,n}^* &= L_{k,n}^* \times \frac{\Delta L_{k,n}}{L_k^*}, \tag{48}
\end{align*}
\]

where \( \Delta L_{k,n} \) is the number of additional parameters allocated to subcarrier \( n \) for uploading, which is proportional to its currently uploaded number of parameters \( L_{k,n}^* \). In (48), the allocation of subcarriers \( \{ C_{k,n}^* \} \) and the channel capacities \( \{ R_{k,n}^* \} \) of the assigned subcarriers remain the same. Two concerns motivate us to design the joint SUPPORT scheme as (48). First, the assigned subcarrier that can currently upload
more updates of parameters can upload more additional parameters in the same additional latency. Second, the proportional additional parameter allocation together with the unchanged allocation of subcarriers and channel capacities can yield a simple upper bound of the additional latency for each worker, as shown in the following lemma.

**Lemma 9 (Additional Latency).** Consider an arbitrary worker, say worker \( k \), the design in (48) results in an upper bound of the minimum additional latency:

\[
\Delta T_k \leq T^* \times \frac{\Delta L^*_k}{L^*_k},
\]

where \( T^* \) is the solved latency in Step 1), \( \Delta T_k \), \( \Delta L^*_k \), and \( L^*_k \) are the additional latency, the number of additional parameters after the rounding operation, and the solved number of parameters in Step 1) of worker \( k \), respectively.

The proof of Lemma 9 is straightforward and hence omitted. Two observations can be made from Lemma 9. On one hand, as mentioned in Example 1, the size of the subproblems are far smaller than the problems of \( \mathbf{W} \)-stage and \( \mathbf{Z} \)-stage, i.e., \( \Delta L^*_k \ll L^*_k \). Therefore, the additional latency \( \Delta T_k \) is small for all workers. On the other hand, the round-up indicator, denoted as \( I_k \), should be the ratio \( I_k = \frac{\Delta L^*_k}{L^*_k} \).

2) **Parameter Rounding Scheme:** Note that Lemma 9 only gives the additional latency for one worker. To minimize the additional one-round latency, the rounding scheme is designed to make the workers with least \( I_k \) to round up and the others to round down, described as follows.

1) Sort the round-up indicators \( \{I_k\} \) from the least to the biggest and the new permutation is indexed by \( k' \), i.e., \( \{I_{k'}\} \) is sorted from the least to the largest.

2) Find the least \( K' \) following the new permutation \( \{I_{k'}\} \), which satisfies

\[
\sum_{k' = 1}^{K'} \Delta L^*_{k'} \geq \sum_{k' = K' + 1}^{K} \Delta L^*_{k'},
\]

where \( L^*_{k'} \) is the additional number of parameters of worker \( k' \) when being rounded up and \( \Delta L^*_{k'} \) is the reduced number of parameters when being rounded down. (50) means that by rounding up \( K' \) workers with least round-up indicators, the parameters of all workers can satisfy Granularity Constraints 1 and 2.

3) The additional one-round latency is \( \Delta T \leq T^* \times I_{K'2} \), where \( T^* \) is the solved one round latency without considering Granularity Constraints 1 and 2.

**VI. EXPERIMENTAL RESULTS**

**A. Experiment Setup**

The experimental settings are specified as follows unless specified otherwise. In the OFDM based PARTEL system, there are \( K \) workers and \( N \) subcarriers. The bandwidth of each subcarrier is \( B = 312.5 \) kHz. The subcarrier channel gains \( \{H_{k,n}\} \) are assumed to be i.i.d. Rayleigh fading with the average path loss of \( 10^{-3} \). The noise power density is set as \( 10^{-9} \) W/Hz. The workers’ computation speeds \( \{f_k\} \) are uniformly selected from the set \( \{0.1, 0.2, \ldots, 1.0\} \times 10^6 \) parameters processed per second in one local computation iteration. The corresponding computation power factors \( \{g_k\} \) are uniformly selected from the set \( \{0.1, 0.2, \ldots, 1.0\} \times 10^{-16} \). The maximum power consumed by workers \( \{P_k\} \) is set as 8 W. Both decomposable models and DNN models are trained using the PARTEL framework. The learning settings are as follows.

- **Decomposable Model:** A \( L_1 \)-regularized logistic regression task is considered, which trains a news-filtering model using the News20 dataset collected in [28]. The model size is \( 1.24 \times 10^6 \). The training and test datasets have 15936 and 3993 samples respectively. \( K = 50 \) workers with \( N = 80 \) subcarriers are used to complete the task.

- **DNN Model:** The CNN model “LeNet-5” proposed in [25] is trained on the MNIST dataset. In the “LeNet-5” model, there are are 60,000 parameters in total. The method of auxiliary variables in [6] is used to train the “LeNet-5” model at the PARTEL framework. In each training iteration, a mini batch of 50 data samples is used. There are 469, 400 auxiliary variables in total. \( K = 30 \) workers with \( N = 50 \) subcarriers are used to complete the task.

For comparison, three communication schemes are considered, described as follows.

- **Joint SUPPORT:** The joint schemes of SUPPORT proposed in Sections IV or V.
- **Baseline:** The number of parameters computed by each worker is first allocated proportional to their computation capacity. Then, the subcarriers are allocated, which is a special case of the joint SUPPORT scheme.
- **Greedy Scheme for FEEL:** The training samples are equally distributed among workers. Thereby, the computation latency and energy of each worker is determined. The subcarrier allocation follows a greedy way. The subcarriers are randomly indexed and sequentially allocated from the 1st to the \( N \)-th. The \( \nu \)-th subcarrier is allocated to the worker whose latency is currently the longest. Note that the latency minimization of one worker given the subcarrier allocation is simple and omitted.

**B. Decomposable Models**

The learning performance of training the logistic regression model is compared in Fig. 2. As observed, the model trained in PARTEL with the proposed joint SUPPORT converges much faster than the one trained in FEEL with the greedy communication scheme, in which each worker uploads the updates of all parameters. Besides, the joint SUPPORT outperforms the baseline in terms of model convergence with a latency reduction of 31.06% on average. That’s because the allocations of parameters and subcarriers are sequentially designed in the baseline.

Fig. 3 shows the impacts of number of workers and subcarriers on the per-round latency. As observed, the per-round latencies of both schemes decrease as the number of workers or subcarriers increases. The reasons are as follows. More workers can provide more computation capacity and hence
reduce the computation latency. Moreover, more subcarriers allocated to workers can reduce the uploading latency.

C. DNN Models

The learning performance of training the LeNet-5 is compared in Fig. 4. In the figure, for FEEL, the optimizer is Adam (proposed in [29]) and the corresponding learning rate is 0.002. For PARTEL, the optimizer is SGD. The learning rates for updating weights and auxiliary variables are 0.002 and 1, respectively. Although two stages (rounds) complete one training iteration in PARTEL using the joint scheme, it outperforms the FEEL using the greedy scheme in terms of model convergence, as the latter has to upload the updates of all parameters in each round. Besides, the joint SUPPORT can reduce latency by 42.11% compared to the baseline for the similar reason in the decomposable model case.

The impacts of the number of workers and subcarriers on the latency performance of training LeNet-5 is compared in Fig. 5. As shown in the figure, the latencies of the two schemes for both W-stage and Z-stage decrease with the number of workers and subcarriers for the same reasons in the case of decomposable models.

The experimental results above show that our proposed joint scheme of SUPPORT has the best performance regarding learning latency and verifies our analysis.

VII. CONCLUDING REMARKS

In this paper, we have presented a set of algorithms for jointly controlling parameter and sub-carrier allocation and power control, which can significantly reduce the latency of PARTEL deployed in a broadband system. This work opens several interesting directions. One is to take the device scheduling into consideration for further accelerating the training process. In the context of PARTEL, it is useful to jointly design the scheduler and parameter allocation. Another interesting direction is to jointly control parameter allocation
and local computation (e.g., processor speeds). In addition, the current joint design assuming OFDM can be extended to other advanced communication techniques such as non-orthogonal multi-access, massive MIMO, and over-the-air aggregation.

**APPENDIX**

**A. Proof of Lemma 2**

KKT conditions are used to show Lemma 2. The Lagrange function of Problem (P1) is

$$L = T + \sum_{n=1}^{N} \mu_n \left(1 - \sum_{k=1}^{K} C_{k,n}\right) + \lambda \left(L - \sum_{k=1}^{K} L_k\right) + \sum_{k=1}^{K} \nu_k \left(L_k - \sum_{n=1}^{N} C_{k,n} L_{k,n}\right) + \sum_{k=1}^{K} \alpha_k \left(E_k - P_k\right) + \sum_{k=1}^{K} \sum_{m=1}^{\text{com}} \beta_{k,m} C_{k,n} \left(T_{k,m}^{\text{com}} - T\right),$$

where \(\{\mu_n\}\), \(\lambda \geq 0\), \(\{\nu_k \geq 0\}\), \(\{\alpha_k \geq 0\}\), and \(\{\beta_{k,m} \geq 0\}\) are multipliers.

Then, consider an arbitrary subcarrier-allocation scheme, where the values of \(\{C_{k,n}\}\) can be either binary or relaxed in the range of \([0, 1]\), KKT conditions are necessary to solve the problem. Some related KKT conditions are given below:

$$\frac{\partial L}{\partial T} = 1 - \sum_{k=1}^{K} \sum_{n=1}^{N} C_{k,n} \beta_{k,n} = 0, \quad 1 \leq k \leq K,$$

$$\frac{\partial L}{\partial L_k} = -\lambda + \nu_k + \alpha_k \frac{\partial (E_k/T_k)}{\partial L_k} + \sum_{n=1}^{N} C_{k,n} \beta_{k,n} \frac{\partial T_{k,m}^{\text{com}}}{\partial L_k} = 0,$$

$$\frac{\partial L}{\partial C_{k,n}} = -\nu_k C_{k,n} + \alpha_k \frac{\partial (E_k/T_k)}{\partial C_{k,n}} + C_{k,n} \beta_{k,n} \frac{\partial T_{k,m}^{\text{com}}}{\partial C_{k,n}} = 0,$$

$$\frac{\partial L}{\partial \beta_{k,m}} = \alpha_k \frac{\partial (E_k/T_k)}{\partial \beta_{k,m}} + C_{k,n} \beta_{k,n} \frac{\partial T_{k,m}^{\text{com}}}{\partial \beta_{k,m}} = 0, \quad \forall (k,n).$$

From the first condition above, we can show that \(\exists \beta_{k,m} \neq 0\).
the above constraint is a convex set. Besides, the objective function and other constraints are convex. Thus, Problem (P4) is convex.

REFERENCES


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